

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	170969	(control\$4 near2 memory)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:32
L2	15677	(control\$4 near2 memory) and ((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) and ((correct\$4 or detect\$4) near3 error\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:35
L3	11391	(control\$4 near2 memory) and ((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) and ((correct\$4 or detect\$4) near3 error\$1) and storage	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:35
L4	5113	(memory adj2 controller) and ((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) and ((correct\$4 or detect\$4) near3 error\$1) and storage	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:36
L5	2046	(memory adj2 controller) and ((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) and (correct\$4 near3 error\$1) and (detect\$4 near3 error\$1) and storage	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:37
L6	1257	(memory adj2 controller) and (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) same storage) and (correct\$4 near3 error\$1) and (detect\$4 near3 error\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:38
L7	135	(memory adj2 controller) and (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) same storage) same (correct\$4 near3 error\$1) same (detect\$4 near3 error\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:38
L8	135	(memory adj2 controller) and (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) same storage) same (correct\$4 near3 error\$1) same (detect\$4 near3 error\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:39

L9	65	(memory adj2 controller) same (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) same storage) same (correct\$4 near3 error\$1) same (detect\$4 near3 error\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:39
L10	17	(memory adj2 controller) same (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) same storage) same (correct\$4 near3 error\$1) same (detect\$4 near3 error\$1)) same read\$4 same writ\$5	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:41
L12	39	(memory adj2 controller) same (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device)) same storage) same ((correct\$4 near3 error\$1) or ECC) same (detect\$4 near3 error\$1) or EDC) same read\$4 same writ\$5	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:42
L13	39	(memory adj2 controller) same (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device) or (control adj2 circuit)) same storage) same ((correct\$4 near3 error\$1) or ECC) same (detect\$4 near3 error\$1) or EDC) same read\$4 same writ\$5	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:43
L15	30	((memory adj2 controller) same (((control adj1 unit) or (control adj1 means) or controller or (control adj1 device) or (control adj2 circuit)) same storage) same ((correct\$4 near3 error\$1) or ECC) same (detect\$4 near3 error\$1) or EDC) same read\$4 same writ\$5) and (sequen\$6 or subsequen\$5)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/09 14:44